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PATENTS

US PAT. # 4,396,883	“Bandgap Reference Voltage Generator”
US PAT. # 5,646,513	“Dynamic Loop Compensation For Continuous-Mode Switching Power Converters”
US PAT. # 5,142,165	“Power On/Off Delay Circuit To Prevent Lock-out”
US PAT. # 5,686,820	“Voltage Regulator With A Minimal Input Voltage Requirement”
US PAT. # 5,528,137	“Network Sensitive Pulse Generator”
US PAT. # 5,596,280	“Apparatus And Method For Testing Circuits By The Response Of A Phase-Lock-Loop”
US PAT. # 5,539,306	“Method And Apparatus For Testing Electrical And Electronic Circuits”
US PAT. # 5,355,100	“Battery Powered Magnetic Pen With Time Out To Prevent Accidental Battery Discharge”
US PAT. # 5,345,197	“Battery Powered Magnetic Pen Having An Oscillator With Improved Efficiency And Low Power Consumption”
US PAT. # 5,523,970	“Non-Volatile Memory Utilizing A Thin-Film, Floating-Gate, Amorphous Transistor”
US PAT. # 5,463,339	“Amorphous Thin-Film Transistor Driver/Receiver Circuit With Hysteresis”
US PAT. # 5,502,673	“Non-Volatile Register System Utilizing Thin-Film Floating-Gate Amorphous Transistors”
US PAT. # 5,543,791	“Non-Volatile Parallel-To-Serial Converter System Utilizing Thin-Film Floating-Gate Amorphous Transistors”
US PAT. # 5,557,272	“Non-Volatile Serial-To-Parallel Converter System Utilizing Thin-Film Floating-Gate Amorphous Transistors”
US PAT. # 6,093,930	“Automatic Probe Replacement In A Scanning Probe Microscope”

PUBLICATIONS AND PAPERS

Thirty-Three Technical Articles Published in the “IBM Technical Disclosure Bulletin”

Two Papers Presented At IBM Technical Conferences